Department of Electrical and Electronics Engineering

Name	**T.Murugan**	
Date of Birth	25.07.1984	
Unique ID	*1-7330840951*	
Educational Qualifications	*M.E, (Ph.d)*	
Designation	Assistant Professor	
Email ID	muruganvlsi@gmail.com	
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Experience	Teaching	Total
	16 Years	16 Years
Date of Joining the Institution	20.05.2019	
Area of Specialization	***Image Processing, VLSI Design***	
Courses taught	Microprocessor and Microcontroller programming, Digital Logic Circuits, Embedded C-programming	
Research Focus	Deep learning, Machine Learning, Image Processing, VLSI Design	
Subject Competency	Microprocessor and Microcontroller programming, Digital Logic Circuits, Embedded C-programming	
No. of papers published	International Journals	Conferences
	3	*1*
PG Specialization	VLSI Design	
Ph.D. Specialization	Information and Communication Engineering	
Projects Carried out	*UG – 10, PG - 2*	
Patents (Filed & Granted)	1	

Academic Credentials

Level	Degree	Specialization	University	Year of Completion
UG	B.E.,	EEE	Anna University, Chennai	2005
PG	M.E.,	VLSI Design	Anna University, Chennai	2007
Ph.D.	Ph.D.,	Pursuing	Anna University, Chennai	Pursuing

Details of Journal Publication:
1. **T.Murugan**, S.Persis Sophia, "FPGA Implementation of Low Power ECG based processor for predicting ventricular Arrhythmia", International Journal of Emerging Technology in VLSI & System on Chip (IJETVSOC), Volume 1 Issue 1, pp-10-15, January 2017
2. **T.Murugan**, V.Vembuselvi , "An efficient technique for illumination adjustment using CLAHE algorithm", International journal of innovative research in computer and communication engineering (IJIRCCE), Volume 4, Issue 3, pp. 4001-4006, March 2016
3. **T.Murugan,** S.Suvitha, **"**Low transient to exploit same tag bits to improve error protection in the caches using ETA", International journal of innovative research in science, engineering and technology (IJIRSET) volume 5, special issue 2, pp.152-157, March 2016

Details of Conference attended:
1. **Murugan T**, Shangith G, "Analysis design and control of switching capacitor based buck boost converter", Emerging Trends in Information & Communication Technologies (ETICT 2022).

Details of Book Chapter and Books Published:
1. G. Dhanabalan and **T. Murugan** "FPGA Design of SAR Type ADC Based Analog Input Module for Industrial", Lecture Notes in Electrical Engineering Applications, Advances in VLSI and Embedded Systems, Springer Nature Singapore Pte Ltd. 2021, https://doi.org/10.1007/978-981-15-6229-7_8

Details of Patents Filed and Granted:
1. Title: Efficient Drone System for Garbage Cleaning in stadiums and Large Outdoor Grounds. Application No.: 202541041208